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APPLICANTS

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** CONTINUING DATA ***** *MT*

This application is a DIV of 09/935,641 08/24/2001 PAT 6,677,609
 which is a DIV of 09/488,038 01/19/2000 PAT 6,333,520
 which is a DIV of 09/011,998 02/23/1998 PAT 6,084,248
 which is a 371 of PCT/JP97/02233 06/27/1997

** FOREIGN APPLICATIONS ***** *MT*

JAPAN 8-170464 06/28/1996

JAPAN 8-211904 ~~07/20/1996~~ *07/23/1996*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>M-T</i> Examiner's Signature Initials	JAPAN	33	7	6

ADDRESS

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TITLE

Thin film transistor, manufacturing method thereof, and circuit and liquid crystal display device using the thin film transistor

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